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1. [\[PDF\] I2C Noise Glitch Filtering - TAS3103A **Glitch Filter** for ILC Lines](#)

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Digital Audio/Video. To provide glitch noise immunity to a device that does not have I2C **glitch filters**, or to increase the glitch ...

focus.ti.com/cn/lit/an/slea053/slea053.pdf

2. [Gate efficient **digital glitch filter** for multiple input ...](#)

A **digital glitch filter** includes first and second NOR gates coupled with feedback ...

Download **PDF** 5185537 · **PDF** help. Primary Examiner: Howell, Janice A. ...

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3. [Digital glitch filter - Patent EP0984556](#)

A **glitch filter** processes overlapping time sequences of an odd number, N, of potentially corrupted ... **Digital glitch filter**. European Patent Application EP0984556.

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4. [Gate efficient **digital glitch filter** for multiple input ...](#)

Typically, a **digital glitch filter** includes two stages of sampling and a comparison between the two View Patent Images (**PDF**) (Registered users only) ...

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5. [Switch Debouncer and **Glitch Filter** with PSoC® 3 and 5 - AN60024 ...](#)

Apr 8, 2010... note introduces the concepts of switch debouncing and **glitch filtering** for **digital** input signals. Several different switch debounce and **glitch filter** designs are

discussed; ... AN60024.pdf, English, 743 KB, 04/06/10 ...

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6. [\[PDF\] ADM1064 Super Sequencer with Voltage Readback ADC Data Sheet \(Rev. C\)](#)

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Digital Glitch Filter. 0 μ s. Minimum programmable filter length. 100 μ s. Maximum programmable filter length. ANALOG-TO-DIGITAL CONVERTER. Signal Range ...

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7. [\[PDF\] 170MHz Triple Video Digitizer with Digital PLL - X98017](#)

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Disable **Glitch Filter**. 0: HSYNC/VSNC **Digital Glitch Filter** Enabled (default). 1:

HSYNC/VSNC **Digital Glitch Filter** Disabled. 0x04. SOG Slicer (0x08) ...

[www.intersil.com/data/fn/fn8218.pdf](#) - [Similar](#)

8. [\[PDF\] Synchronous Resets? Asynchronous Resets? I am so confused! How ...](#)

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will work to **filter** out the **glitches**, but it is ugly! This solution requires that a **digital** delay (meaning the delay will vary with temperature, voltage and ...

[www.sunburst-design.com/.../CummingsSNUG2002SJ_Resets.pdf](#) - [Similar](#)

9. [Pulse and Direction for the Position and Velocity Commands ...](#)

The **glitch filter** has a **digital** delay line that samples four time points on the ...

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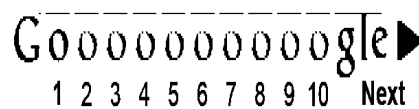
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10. [\[PDF\] USBN9603 USBN9604 Universal Serial Bus Full Speed Node Controller ...](#)

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clock recovery circuit, a **digital glitch filter**, End Of Packet (EOP) detection circuitry, and bit stuffing and unstuffing logic. The ...

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